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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUL 14 2005

Applicant: Darren L. Anand et al.)
Serial No.: 10/604,621) Group Art Unit: 2819
Filed: August 5, 2003) Before the Examiner: A. TRAN
For: SYSTEM AND METHOD FOR)
IMPLEMENTING SELF-TIMED)
DECODED DATA PATHS IN)
INTEGRATED CIRCUITS)

STATUS REQUEST

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Please advise as to the status of the above-identified application, as an Request for Continued Examination (RCE) and Information Disclosure Statement (IDS) were filed on January 10, 2005. We have not received any further correspondence from the Patent Office and according to PAIR the case is still allowed.

If there are any charges with respect to this Status Request or otherwise, please charge them to Deposit Account No. 09-0456 maintained by applicants' assignee.

Respectfully submitted,

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I certify that this correspondence is being sent to the Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 via fax to 703-872-9306 on:	
July 14, 2005	
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Jessie L. Hester	
(Name of Person Mailing Paper)	
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Signature	Date

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